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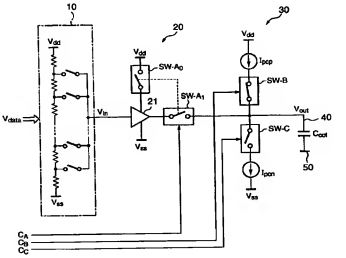
(54) Title: DISPLAY DEVICE DRIVING CIRCUIT



(57) Abstract: The present invention provides a driving circuit capable of reducing power consumption in an amplifier for outputting a target voltage. A driving circuit for driving a capacitive load Ccol of a display device, comprising: driving signal supplying means (10) for supplying a driving signal Vin having a target voltage to be applied; an amplifying stage (20) for receiving the driving signal Vin and selectively outputting the driving signal Vin to the capacitive load Ccol; and a pair of current sources Ipcp, Ipcn for selectively supplying a positive current and a negative current to the capacitive load Ccol, respectively during their on-states. The driving circuit repeats a repetitive operation including a pre-operation where any one of the current sources Ipcp, Ipcn is switched ON in accordance with the driving signal Vin and then switched OFF and a post-operation where the amplifying stage (20) is switched to a state for outputting the driving signal Vin to the capacitive load Ccol after the pre-operation.

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